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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/865,528	05/29/2001	Keizo Yamada	NEC-F92/USA	4338

7590 06/04/2003

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EXA	MINER
NGUYE	EN, VINH P
ART UNIT	PAPER NUMBER

DATE MAILED: 06/04/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)			
Office Action Summary		09/865,528 YAMADA, KEIZO				
		Examiner	Art Unit			
	•	VINH P NGUYEN	2829			
-	- The MAILING DATE of this communication app	1				
Period for Reply						
THE N - Exten after S - If the - If NO - Failur - Any re	DRTENED STATUTORY PERIOD FOR REPLY MAILING DATE OF THIS COMMUNICATION. stons of time may be available under the provisions of 37 CFR 1.13 SIX (6) MONTHS from the mailing date of this communication. period for reply specified above is less than thirty (30) days, a reply period for reply is specified above, the maximum statutory period to reply within the set or extended period for reply will, by statute, sply received by the Office later than three months after the mailing d patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be time within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from to cause the application to become ABANDONET.	ely filed will be considered timely. the mailing date of this communication. 35 U.S.C. § 133).			
1)⊠	Responsive to communication(s) filed on the	amendment filed on 03/12/03 .				
2a) 🗌	This action is FINAL. 2b)⊠ This	is action is non-final.				
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Dispositi	on of Claims					
•	4) Claim(s) 1-26 is/are pending in the application.					
4a) Of the above claim(s) <u>2-13 and 15-25</u> is/are withdrawn from consideration.						
5)	Claim(s) is/are allowed.					
6)🛛	Claim(s) <u>1,14 and 26</u> is/are rejected.					
7)	Claim(s) is/are objected to.					
8) Claim(s) are subject to restriction and/or election requirement. Application Papers						
9) 🗌 T	he specification is objected to by the Examine	r.				
10) 🔲 T	he drawing(s) filed on is/are: a)□ accep	ted or b)⊡ objected to by the Exan	niner.			
	Applicant may not request that any objection to the	e drawing(s) be held in abeyance. Se	ee 37 CFR 1.85(a).			
11) 🔲 T	he proposed drawing correction filed on	is: a)☐ approved b)☐ disapprov	ved by the Examiner.			
If approved, corrected drawings are required in reply to this Office action.						
12) 🗌 T	he oath or declaration is objected to by the Exa	aminer.				
Priority under 35 U.S.C. §§ 119 and 120						
13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).						
a)[☐ All b) ☐ Some * c) ☐ None of:					
	 Certified copies of the priority documents 	s have been received.				
	Certified copies of the priority documents	s have been received in Application	on No			
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 						
14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).						
a) The translation of the foreign language provisional application has been received. 15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.						
Attachment	(s)	•				
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s) 4) Interview Summary (PTO-413) Paper No(s) 5) Notice of Informal Patent Application (PTO-152) 6) Other:						
S. Patent and Tra	demark Office					

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1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

2. Claims 1 and 14 (insofar as understood) are rejected under 35 U.S.C. 103(a) as being unpatentable over Kawamoto et al (Pat # 5,453,994).

As to claims 1 and 26, Kawamoto et al disclose a test system as shown in figure 1 having a test sample (1) to be tested, current measuring means (3,4b) for measuring current generated in the sample by the beam irradiation (7) from beam irradiating means (not shown) and comparator means (5c) for comparing the measured current signal from the current measuring means (3,4b) with expected values (5b) (read as second test sample) in order to determine if there is an existed OBIC signals. It is noted that the device of Kawamoto et al does not mentioned about the memory means, the position on the test sample at which a difference exits and the scanning second test sample. However, it would have been obvious for one of ordinary skill in the art to recognize that the comparator (5c) would inherently have memory means for storing the measured current signals in order to perform its task. Furthermore, by observing the output from the comparator, one of ordinary skill in the art would also recognize that the position at which the difference exits. Giedd teaches that it would have been well known for one of ordinary skill in the art to provide test signals to both physical logic under test (20) and a reference logic (30) and to use the comparator (40) for comparing the differences. It would

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have been obvious for one of ordinary skill in the art to considered that the expected values (5b) could come from a physical reference test sample as taught by Giedd since this is an alternative way to obtain the expected values by using a physical reference device.

3. Claim 14 is rejected under 35 U.S.C. 103(a) as being unpatentable over Kawamoto et al (Pat # 5,453,994).

As to claim 14, Kawamoto et al disclose a test system as shown in figure 1 having a test sample (1) to be tested, current measuring means (3,4b) for measuring current generated in the sample by the beam irradiation (7) from beam irradiating means (not shown) and comparator means (5c) for comparing the measured current signal from the current measuring means (3,4b) with expected values (5b) (read as second test sample) in order to determine if there is an existed OBIC signals. It is noted that the device of Kawamoto et al does not mentioned about the memory means, the position on the test sample at which a difference exits and the scanned second test sample. However, it would have been obvious for one of ordinary skill in the art to recognize that the comparator (5c) would inherently have memory means for storing the measured current signals in order to perform its task. Furthermore, by observing the output from the comparator, one of ordinary skill in the art would also recognize that the position at which the difference exits. Giedd teaches that it would have been well known for one of ordinary skill in the art to provide test signals to both physical logic under test (20) and a reference logic (30)

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and to use the comparator (40) for comparing the differences. It would have been obvious for

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one of ordinary skill in the art to considered that the expected values (5b) could come from a

physical reference test sample as taught by Giedd since this is an alternative way to obtain the

expected values by using a physical reference device.

It is noted that the reference logic of Giedd is not CAD data. It is a physical device.

The prior art made of record and not relied upon is considered pertinent to applicant's 4.

disclosure.

Shida et al (Pat # 5,757,198) disclose method and apparatus for detecting an IC defect

using charged particle beam.

Any inquiry concerning this communication or earlier communications from the examiner 5.

should be directed to VINH P. NGUYEN whose telephone number is (703) 305-4914.

Any inquiry of a general nature or relating to the status of this application or proceeding should

be directed to the Group receptionist whose telephone number is (703) 305-4900.

05/23/03